4. (Amended) A voltage generating/transferring circuit according to claim 1, wherein a second oscillation signal is input to an even-numbered boost unit from the input node, a third oscillation signal is input to an odd-numbered boost unit from the input node, and the second and the third oscillation signals have opposite phases or different timings.

11. (Amended) A voltage generating/transferring circuit comprising:

a boost unit group including a plurality of boost units series-connected between input and output nodes;

a first transistor connected between the input node and a node for receiving a first voltage; and

a first capacitor having one end which is connected to the output node, and another end which receives a first oscillation signal,

wherein each of the boost units has input and output portions, a second transistor having a gate and a drain connected to the input portion and a source connected to the output portion, and a second capacitor in each of the boost units connected to the input portion, a charge moves between the output portion of one of the boost units and the input portion of another of the boost units, and a gate of said first transistor is connected to the input portion of one of the boost units.

14. (Amended) A voltage generating/transferring circuit according to claim 11, wherein a second oscillation signal is input to an even-numbered boost unit from the input node, a third oscillation signal is input to an odd-numbered boost unit from the input node, and the second and the third oscillation signals have opposite phases or different timings.

21. (Amended) A voltage generating/transferring circuit comprising:

a boost unit group including at least a first boost unit and a second boost unit series-connected between input and output nodes;

a first transistor connected between the input node and a node for receiving a first voltage; and

a first capacitor having one end which is connected to the output node, and another end which receives a first oscillation signal,

wherein each of said first and second boost units has an input portion, an output portion, a second transistor having both a gate and drain connected to the input portion and a source connected to the output portion, and a second capacitor in each of said first and second boost units connected to the input portion, the source of the second transistor of said first boost unit being directly connected to the input portion of said second boost unit, and a gate of said first transistor being connected to the input portion of one of said first and second boost units.

Kindly add the following new claims:

23. (New) A voltage generating/transferring circuit according to claim 1, wherein the first oscillation signal and an oscillation signal which is input to the boost unit connected to the first capacitor have opposite phases or different timings.

24. (New) A voltage generating/transferring circuit according to claim 11, wherein the first oscillation signal and an oscillation signal which is input to the boost unit connected to the first capacitor have opposite phases or different timings.